**IEEE 13 Bus system in Simulink**

* Lines are modelled as Three Phase and Single Phase Pi Section Line
* Voltage regulators are modelled and includes LDC
* Runs in discrete domain with Ts=50 µsec
* ZIP loads are modeled

**Regulator Taps and Parameters**

|  |  |  |  |
| --- | --- | --- | --- |
| Regulator ID: | 1 |  |  |
| Line Segment: | 650 - 632 |  |  |
| Phases: | A - B -C |  |  |
| Connection: | 3-Ph,LG |  |  |
| Monitoring Phase: | A-B-C |  |  |
| Bandwidth: | 2.0 volts |  |  |
| PT Ratio: | 20 |  |  |
| Primary CT Rating: | 700 |  |  |
| Compensator Settings: | Ph-A | Ph-B | Ph-C |
| R - Setting: | 3 | 3 | 3 |
| X - Setting: | 9 | 9 | 9 |
| Voltage Level (Vref): | 122 | 122 | 122 |

**Comparison of Steady state voltages and Substation Power**

|  |  |  |  |
| --- | --- | --- | --- |
| **Substation Active Power (KW)** | **Phase a** | **Phase b** | **Phase c** |
| Benchmark | 1251.398 | 977.332 | 1348.461 |
| Simulink | 1253 | 979.6 | 1348 |

|  |  |  |  |
| --- | --- | --- | --- |
| **Substation Reactive Power (KVar)** | **Phase a** | **Phase b** | **Phase c** |
| Benchmark | 681.570 | 373.418 | 669.784 |
| Simulink | 694.7 | 378.2 | 676.9 |

With ZIP load modeling the substation power is very close to the benchmark values.

|  |  |
| --- | --- |
| **Tap Setting** | **Regulator 1** |
| Benchmark | 10 8 11 |
| Simulink(Initial) | 10 8 11 |
| Simulink(Final) | 10 8 10 |

**Phase A Voltage**

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**Phase B Voltage**

****

**Phase C Voltage**

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**% Error Voltage**

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|  |  |  |  |
| --- | --- | --- | --- |
| **Max Error pu** | **Phase a** | **Phase b** | **Phase c** |
|  | 0.0061 | 0.0062 | 0.0038 |